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501.40792vX1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

YAMAGUCHI et al.

Serial No.:

Not yet assigned

Filed:

November 30, 2001

For:

SEMICONDUCTOR MODULE AND METHOD OF MAKING

THE DEVICE

UNDER 37 CFR \$1.97 & 1.98

Assistant Commissioner for Patents Washington, D.C. 20231 November 30, 2001

Sir:

In the matter of the above-identified application, this Information Disclosure Statement is being submitted with the following citation as specified in 37 CFR §1.97(d).

"A copy of any patent, publication or other information listed in an Information Disclosure Statement is not required to be provided if it was previously cited by or submitted to the Office in a prior application, provided that the prior application is properly identified in the statement and relied upon for an earlier filing date under 35 U.S.C. §120."

Applicants are submitting herewith a copy of Form PTO1449 which lists documents cited in parent applications Serial
No. not yet assigned, being filed on November 30, 2001.

It is respectfully requested that this information disclosure statement be considered by the Examiner. Please

charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus Deposit Account No. 01-2135 (501.40792VX1) please credit any excess fees to such deposit account.

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT						APPLICANT YAMAGUCHI et al.			
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